A3
Concl.

RAM device controller provides a general solution for connecting to the USB by providing an interface to the USB electrical bus and supporting the fixed protocol that is associated with all USB applications. The application specific aspects are handled by writing packets into a common memory and reading packets from the memory under interrupt control.

REMARKS

The markup of the amended paragraphs is attached as Exhibit A. Also attached (as Exhibit B) are Filing Receipts from the related applications as Support for the Serial Numbers being substituted for the docket numbers (54208-28 and 54208-39) referred to in the instant specification. The amendment to the specification embodies merely matters of form and does not introduce new matter.

In the event that a fee is due/overpaid, the Commissioner is hereby authorized to charge/credit any such fees to Deposit Account 02-3964 (Order No. 54208-38).

Respectfully submitted,

Dated: 04/05/02

Anthony B. Diepenbrock
Reg. No. 39,960

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Technology Center 2100

OPPENHEIMER WOLFF & DONNELLY LLP
Customer Number 25696
P.O. Box 10356
Palo Alto, CA 94303
Tel: 650/320-4000
Fax: 650/320-4100

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